

Circuit Area Optimization in Energy Temporal Sparse Scenarios for Multiple Harvester Powered Systems

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Abstract—Multi-source energy harvesters are gaining interest as a robust alternative to power wireless sensors, since the sensor node can maintain its operation regardless of the fact that one of its energy sources might be temporarily unavailable. Interestingly, and less explored, when the energy availability of the energy sources present large temporal variations, combining multiple energy sources reduce the overall sparsity. As a result, the performance of a multiple energy harvester powered sensor node is significantly better compared to a single energy source which harvests the same amount of energy. In this context, a circuit area optimization framework for multiple source energy harvesting powered systems is proposed. This framework takes advantage of this improvement in performance to provide the optimal amount of energy harvesters, the requirements of each energy harvester and the required energy buffer capacity, such that the overall area or volume is minimized. As the results show, by conducting a joint design of the energy harvesters and the energy buffer, the overall area or volume of a sensor node can be significantly reduced.

I. INTRODUCTION

Energy harvesting is a technology which is considered as a firm candidate to enable perpetual wireless sensor networks [1]. Recently, multi-source energy harvesters are gaining interest as a robust alternative to power wireless sensors [2], [3], [4]. As an example, authors in [2] provide a platform for vibration and solar energy harvesting. These platforms are more robust than the single-source ones. Indeed, if a certain energy source renders unavailable for a certain time period, due to the time asynchronicity among energy sources the sensor node can still maintain its normal operation.

One of the main challenges in the design of energy harvesting enabled sensor nodes lies in the dimensioning of both the energy harvesting and energy buffer units. Considering both subsystem units to be sufficiently large solves undesired interruptions during the normal operation of the sensor node and, accordingly, on the wireless network.

Unfortunately, over-dimensioning precludes desirable miniaturization of the sensor nodes, caused by the relatively small power densities of existing ambient energy sources and low energy density of energy buffers [5], [1]. As an example, in order to harvest 0.2 mW vibrational energy and to store 1 J of energy, an energy harvester of approximated 1 cm^2 and an energy buffer of approximated 2 cm^3 would be required.

An additional, but less explored, advantage of heterogeneous multiple-source energy harvesters, which aids the miniaturization of the sensor nodes, is that when the ambient energy

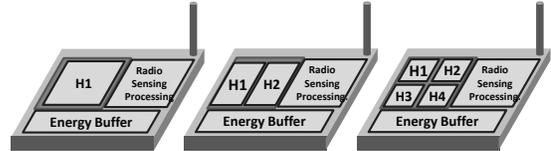


Fig. 1. Multi-source energy harvesting enabled wireless sensors.

presents a large temporal variations (i.e., the harvested power randomly varies over a wide range along time) the combination of multiple statistically independent energy sources lowers the sparsity of the overall energy which is harvested. This causes that sensor nodes which are powered by multi-source energy harvesters outperform in contrast to single-source configurations. Equivalently, the requirements in terms of energy buffer capacity can be relaxed, while maintaining the same performance. As an example, Fig. 1 shows three sensor nodes that implement one, two or four energy harvesters which occupy the same overall area, in a chip-like planar implementation

In this paper we present an analytic framework which optimizes the overall occupied area by the energy harvesting and energy buffer units. In particular, this model accounts for the requirements and capabilities of the sensor units, and provides (i) the optimal number of energy harvesters, (ii) their size and (iii) the energy buffer capacity, such that the overall area of the sensor node is minimized, while still meeting the user-defined requirements of the communications unit.

As a result, in addition to the aforementioned benefits in terms of robustness when using multiple energy harvesters, this framework shows that using multi-source energy harvesters in energetically sparse scenarios results in a significant reduction of the sensor size.

The structure of this paper is organized as follows: In Sec. II we present the sparse energy sources. In Sec. III we compare the performance of single-source to multi-source energy harvesters powered sensor nodes. In Sec. IV we present the circuit-area model to be optimized, while in Sec. V we evaluate this model in a particular case. Finally, in Sec. VI we conclude our work.

II. SPARSE ENERGY SOURCES

Ambient energy is generally generated by the aggregation of an extensive number of physical entities which simultaneously

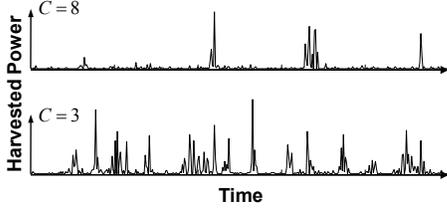


Fig. 2. Harvested power from a sparse ambient source of crest factor of (upper) $C = 8$ and (lower) $C = 3$.

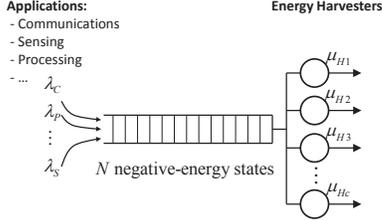


Fig. 3. The negative energy queue model.

radiate power [1]. Then, the random contribution of each entity, in both magnitude and time duration, entails a time-varying character in the aggregated power.

Accordingly, we refer to a sparse energy source as any physical phenomena which produces an aggregated power in a sparse, time-varying manner, such that this power cannot be known or estimated and the magnitude of the instantaneous power falls within a wide range. In fact, sparse energy sources are present in a wide variety of physical phenomena. Among others, acoustic energy, mechanical, vibrational or RF energy [6], [7], [8] are considered representative examples of such sources, when considering a large time scale.

In this work, we propose the power crest factor as a metric to enable the comparison of performance among ambient energy sources. The power crest factor models the relation between the average peak power over the average power. We define the power crest factor as:

$$C = \frac{\overline{P_{peak}}}{P_H}, \quad (1)$$

where $\overline{P_{peak}}$ is the average peak power and P_H refers to the average harvested power. Fig. 2 shows examples of two random energy sources with different crest factor ($C = 8$ and $C = 3$). As it is shown, a large crest factor provides short but powerful bursts of energy, while leaving large inter-burst times where the available energy is far below the average value.

III. MULTIPLE SOURCE ENERGY HARVESTERS

Combining independent energy sources, the sparsity of the overall process is reduced and thus the energy fadings are potentially reduced, as well. In this section we evaluate the improvement on performance that using multiple energy harvesting platforms has when contrasted to single harvester platforms.

A. Energy Model

In order to evaluate this performance, we use the negative energy queue model which is shown in Fig. 3. This Markov-based model is similar to other existing energy models for

energy harvesting [9], [10], [11], [12]. However, as it is shown, the arrivals of this queue are generated by the set of applications of the sensor node. i.e., every time an application spends one unit of energy, it generates an arrival of negative energy. Each kind of application has an associated generation rate in power units (e.g. λ_C for communications, λ_P for processing and λ_S for sensing). On the other hand, each harvester has an associated service time, $T_H = E_s/\mu_H$, which is the time that this energy harvesting unit needs to process one negative energy packet, where E_s is the energy of a negative energy packet and μ_H the energy harvesting rate in power units.

Thus, the number N of negative energy states is related to the energy buffer capacity, C_B as:

$$N = \frac{C_B}{E_s}. \quad (2)$$

Additionally, if at a certain time t_k the queue has L^k negative energy packets, then the energy state s^k at the energy buffer is given by:

$$s^k = C_B - L^k E_s. \quad (3)$$

B. Evaluation Metrics

As the main metrics that we have used to evaluate the performance of the sensor node, we first have defined the energy utilization, ρ_e in Energy-Erlangs (E2), which relates the amount of power which is required, P_C , in contrast to the harvested power, P_H , as:

$$\rho_e = \frac{P_C}{P_H}. \quad (4)$$

Secondly, we define the energy outage probability, p_{out} , as the probability of depleting the energy buffer, and thus temporarily interrupting the communication.

C. Performance of a Multiple Source Energy Harvester

In order to provide realistic results to justify the importance of such platforms, we have considered an average communications rate of $\lambda_c = P_C = 100 \mu\text{W}$. Then, we have considered each negative energy packet to be of $10 \mu\text{J}$. Finally, we have set the overall harvesting rate $N\mu_H = P_H = P_C/\rho$, where ρ_e has been set as an evaluation parameter. Therefore, each harvester harvests an average power of $P_C/\rho_e N$. These energy harvesting rates can be achieved by means of vibrational harvesters [1].

In order to generate the sparse energy sources, we have approximated the ambient energy by a random process generated by exponentially distributed energy bursts of power $P_H C/N$, with an inter-burst time of $0.1/C$ seconds. An exponentially distributed random process has been chosen as it presents the largest entropy, thus estimating the worst case [6].

Fig. 4 and Fig. 5 compare the improvement over p_{out} that using multiple harvesters has as a function of the energy buffer capacity, C_B , for a crest factor of $C = 10$ and $C = 100$ respectively. These results have been obtained by assuming in the negative energy queue model $\rho_e = 0.9$. As it is shown, there is a clear improvement, since varying from one to five harvesters, the energy buffer capacity can be reduced from

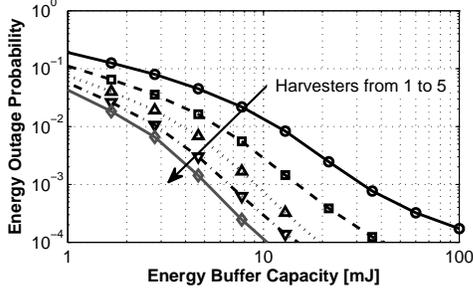


Fig. 4. Energy Outage Probability as a function of the energy buffer capacity. $\rho_E = 0.9$ E2 and $C = 10$.

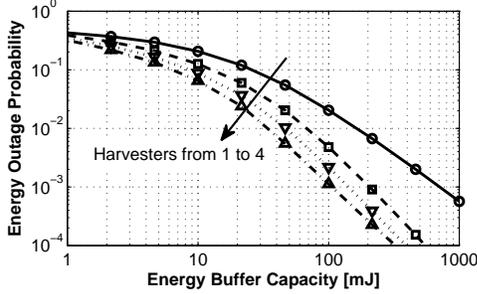


Fig. 5. Energy Outage Probability as a function of the energy buffer capacity. $\rho_E = 0.9$ E2 and $C = 100$.

30 mJ to just 5 mJ and from 600 mJ to just 100 mJ, while still maintaining $p_{out} < 10^{-3}$.

In addition to this, Fig. 7 and Fig. ?? compare this improvement as a function of the ρ_e for crest factors of $C = 10$ and $C = 100$ respectively. In order to obtain these results, the energy buffer capacity has been set to $C_B = 10$ mJ in Fig. 7 and to $C_B = 100$ mJ in Fig. ?. As it is shown, multi-source energy harvesters are able to provide similar performance, but at larger ρ_e values and, therefore, requiring smaller energy harvesting area.

As a result, we observe that multi-source energy harvesters can help reducing both the energy buffer capacity, as well as the energy harvesting requirements, while still providing the required performance.

IV. CIRCUIT AREA MODEL

As seen in the previous section, additional energy harvesters have a positive impact upon the performance. Nonetheless, this technique produces a non-negligible area overhead, since

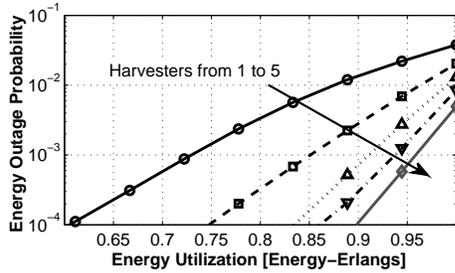


Fig. 6. Energy Outage Probability as a function of the energy utilization. $C_B = 10$ mJ and $C = 10$.

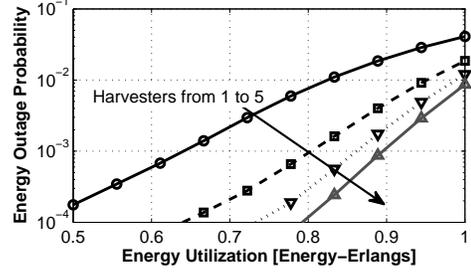


Fig. 7. Energy Outage Probability as a function of the energy utilization. $C_B = 100$ mJ and $C = 100$.

TABLE I
VALUES USED IN THE OPTIMIZATION FRAMEWORK

Parameter	Value	units
A_{H0}	0.02	cm ²
A_{NH}	.01	cm ²
A_{NP}	6.66	cm ² mW ⁻¹

each energy harvester requires some additional circuitry and separation space.

An additional compromise is that low values of ρ_e help reducing the energy buffering capacity at the cost of proportionally increasing the energy harvesting requirements.

These compromises motivate a framework for circuit area optimization which considers the user-defined requirements, the area overhead of multiple harvesters and the energy buffer capacity. In order to do so, we first relate the required power, harvesting power, number of harvesters and energy buffer capacity which are able to achieve the required performance in energy outage probability, through the energy model presented in Sec. III. Afterwards, this is translated into circuit area by means of the following model.

We then define the overall area of the system as:

$$A_{TOTAL} = A_H + A_B + A_A, \quad (5)$$

where A_H refers to the area of the harvesting unit, A_B stands for the area of the energy buffer unit and A_A is the area of the applications units (i.e., processing, sensing and communications unit). In particular, since A_A is fixed and provided by a certain application, A_A is not considered in the following circuit area optimization.

A. Area of the Energy Harvesting Unit

The area of the harvesting unit depends on mainly two factors, the number of energy harvesters and the power that these aim to harvest. In particular, we linearly approximate the area of the energy harvesting unit by:

$$A_H = A_{H0} + A_{HN}N_H + A_{HP}P_R/\rho_e, \quad (6)$$

where A_{H0} refers to a constant area, A_{HN} to the partial contribution of A_H with respect to the number, N_H , of energy harvesters and A_{HP} to the partial contribution of A_H with respect to the required power P_H .

In this work, we have chosen the parameters shown in Table I, which correspond to reasonable values as reported in [1].

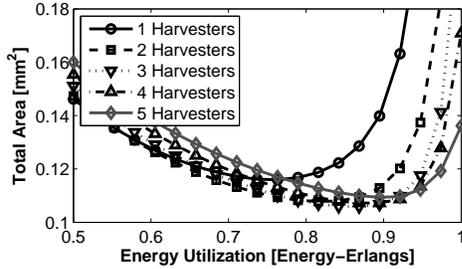


Fig. 8. Overall Area in terms of the Energy Utilization. $C = 10$.

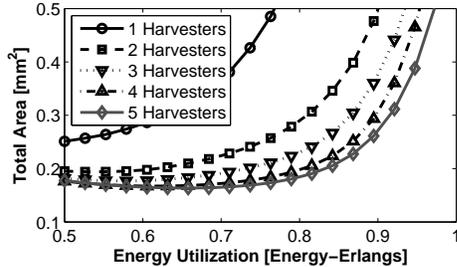


Fig. 9. Overall Area in terms of the Energy Utilization. $C = 100$.

B. Area of the Energy Buffer

In line with recent advancements in energy buffering [5], each technology presents an associated energy density. In this context, we have considered consistent values for this density of $D_B = 2 \text{ J/cm}^{-3}$ and a fixed height of 1 cm. Similar to A_H , we may linearly approximate the overall area of the energy buffer as:

$$A_B = A_{B0} + C_B D_B, \quad (7)$$

where A_{B0} is a fixed area overhead and C_B is the required capacity of the energy buffer in mJ units.

V. AREA OPTIMIZATION

In order to optimize the area, we have simulated the sensor node through the same energy model as described in the previous sections. In addition to this, a target in the energy outage probability has been set to $p_{out} = 10^{-4}$.

Fig. 8 shows the required overall occupied area for the joint energy harvesting and energy buffer unit to meet the user defined requirements in both output power and energy outage probability, when considering a crest factor of $C = 10$. As it is shown, the minimum area is found at a $\rho_E = 0.87 \text{ E2}$, considering three energy harvesters.

Similarly, Fig. 9 shows the results of the circuit-area optimization when considering the same system requirements, but assuming a crest factor of $C = 100$. As it is shown, an increase in the crest factor motivates the use of more energy harvesters. In particular, the minimum area is found at a $\rho_E = 0.66 \text{ E2}$, considering five energy harvesters. The outcome of this design, which are required for the energy harvesting unit and an energy buffer to minimize the area can be found in Table. II for both cases.

VI. CONCLUSION

Multi-source energy harvesting is gaining popularity as an alternative to power wireless sensor nodes. The benefits that

TABLE II
COMPONENT REQUIREMENTS

Crest Factor	Parameter	Value	Units
10	Harvesters	2	—
	Area Harvester (total)	9.8	mm ²
	P_H (each)	57.5	μW
	Area Energy Buffer	2.0	mm ²
	Capacity Energy Buffer	10	mJ
100	Harvesters	5	—
	Area Harvester (each)	14.7	mm ²
	P_H (each)	40	μW
	Area Energy Buffer	4	mm ²
	Capacity Energy Buffer	20	mJ

this alternative provides when the ambient energy is largely time-variant is two-fold: on the one hand, it provides robustness to the sensor node, while on the other hand, the sparsity of the overall contribution is reduced, and thus its performance is improved. In this context, circuit area optimization which takes advantage of the improvement in performance of multiple-source energy harvesters has been addressed. As it has been shown, this joint effort can help reducing the overall area, thus enabling a future miniaturization of the wireless sensor nodes.

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REFERENCES

- [1] S. Sudevalayam and P. Kulkarni, "Energy harvesting sensor nodes: Survey and implications," *IEEE Communications Surveys Tutorials*, vol. 13, no. 3, pp. 443–461, 2011.
- [2] S. Bandyopadhyay and A. Chandrakasan, "Platform architecture for solar, thermal, and vibration energy combining with mppt and single inductor," *IEEE Journal of Solid-State Circuits*, vol. 47, no. 9, pp. 2199–2215, September 2012.
- [3] C. Park and P. Chou, "Ambimax: Autonomous energy harvesting platform for multi-supply wireless sensor nodes," in *Proc. of the 3rd Annual IEEE Communications Society on Sensor and Ad Hoc Communications and Networks (SECON)*, vol. 1, 2006, pp. 168–177.
- [4] A. S. Weddell, M. Magno, G. V. Merrett, D. Brunelli, B. M. Al-Hashimi, and L. Benini, "A survey of multi-source energy harvesting systems," in *Proc. of the Design, Automation Test in Europe Conference Exhibition (DATE)*, 2013, pp. 905–908.
- [5] D. Pech, M. Brunet, H. Durou, P. Huang, V. Mochalin, Y. Gogotsi, P.-L. Taberna, and P. Simon, "Ultrahigh-power micrometre-sized supercapacitors based on onion-like carbon," *Nature Nano*, vol. 5, no. 9, pp. 651–654, September 2010.
- [6] M. Win, P. Pinto, and L. Shepp, "A mathematical theory of network interference and its applications," *Proceedings of the IEEE*, vol. 97, no. 2, pp. 205–230, feb. 2009.
- [7] S. Chalasani and J. Conrad, "A survey of energy harvesting sources for embedded systems," in *Proc. of the IEEE Southeastcon*, 2008, pp. 442–447.
- [8] A. Hajati, S. Bathurst, H. Lee, and S. Kim, "Design and fabrication of a nonlinear resonator for ultra wide-bandwidth energy harvesting applications," in *Proc. of the IEEE International Conference on Micro Electro Mechanical Systems (MEMS)*, January 2011, pp. 1301–1304.
- [9] R. G. Cid-Fuentes, A. Cabellos, and E. Alarcon, "Energy harvesting enabled wireless sensor networks: Energy model and battery dimensioning," in *Proc. of the 7th International Conference on Body Area Networks (BODYNETS)*, September 2012.
- [10] R. Rajesh, V. Sharma, and P. Viswanath, "Information capacity of energy harvesting sensor nodes," in *Proc. of the IEEE International Symposium on Information Theory*, 31 2011–aug. 5 2011, pp. 2363–2367.
- [11] O. Ozel, K. Tutuncuoglu, J. Yang, S. Ulukus, and A. Yener, "Transmission with energy harvesting nodes in fading wireless channels: Optimal policies," *Journal of Selected Areas in Communications*, vol. 29, pp. 1732–1743, September 2011.
- [12] M. Gorlatova, A. Wallwater, and G. Zussman, "Networking low-power energy harvesting devices: Measurements and algorithms," in *Proc. of the IEEE INFOCOM*, april 2011, pp. 1602–1610.